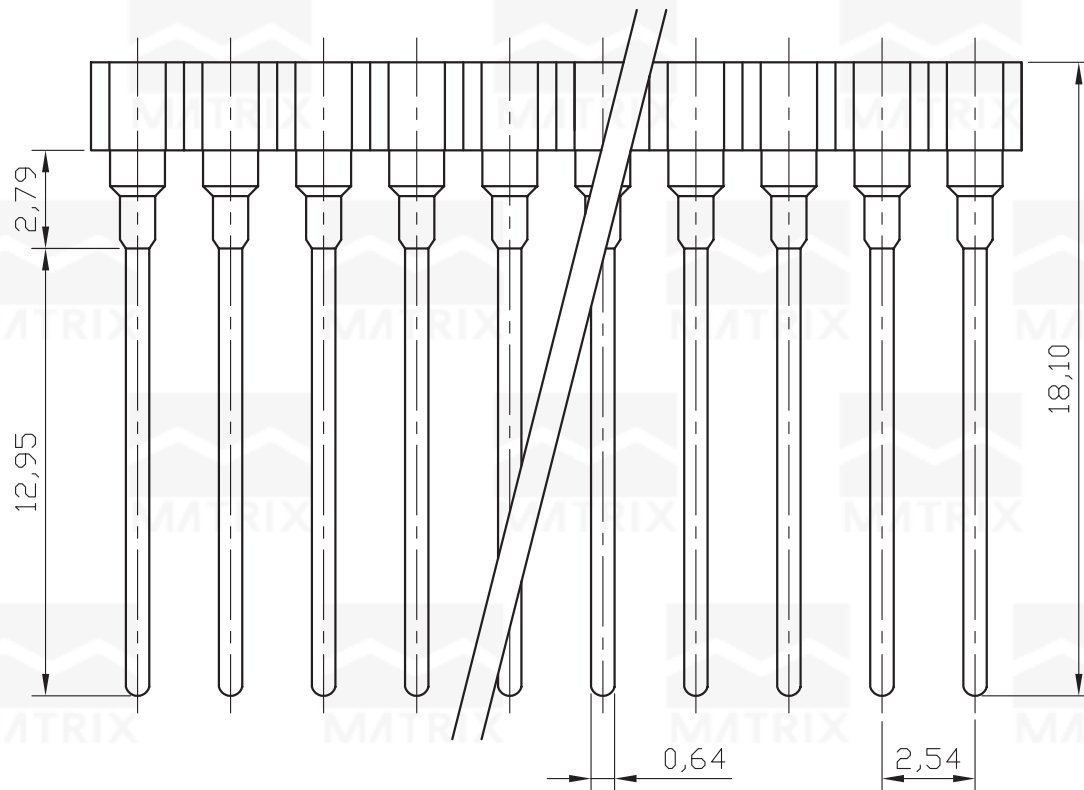

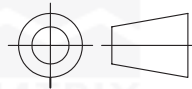


REV	ECN. NO.	APP	DATE
A	EDIÇÃO INICIAL		



HOW TO ORDER:
BSWW-[]-[]-[]-[]-[]
1 2 3 4 5 6

- 1- Num. Ways:
 Single Row: 1x1 to 1x50
 Double Row: 2x1 to 2x40
- 2- Spring Plating:
 (E)-Sn
 (D)-FLASH Au
 (F)-0,38um Au
 (G)-0,76um Au
- 3- Terminal Plating:
 (E)-Sn
 (D)-FLASH Au
 (F)-0,38um Au
 (G)-0,76um Au
- 4- Insulator:
 (N)- HB
 (P)- V0
- 5- (D) Double Row
 () Single Row
- 6- (R) RoHS Compliant

		MATRIX Tecnologia Ind. e Com. Ltda.	
UNIT mm	SCALE 5:1	TITLE Sip Socket Wire Wrap	DATE September 2007
TOLERANCE: ±0.25			APP. BY
		MATL	CHK. BY
		DWG. NO.	DR. BY Diego